

Application No. 09/816,672
Amendment Under 37 C.F.R. §1.111 dated October 20, 2004
Response to the Office Action of July 21, 2004

Amendment to the Title:

Please **Delete** the title and replace with the following new title: A DIGITAL CAMERA
HAVING OVERLAPPED EXPOSURE

Amendment to the Specification:

Amend the paragraph beginning on page 8, lines 9 – 14 to read as follows:

As shown in Figure 5, the ~~vertical~~ horizontal transfer register 18c is also formed by a plurality of metals MH. It is however noted that one metal MH is assigned to the row on which the vertical transfer register 18b is provided and one metal to the row on which the light-receiving element 18a is provided. A drive pulse H1 is applied to the metal MH on the row having vertical transfer register 18b while a drive pulse H2 is to the metal MH on the row having the light-receiving element 18a.

Amend the paragraph beginning on page 13, lines 15 – 25 to read as follows:

The CPU 28 concretely operates according to a flowchart shown in Figure 10. When the power is on, first in step S1 the switch SW1 is connected toward the signal processing circuit 32. Next, it is determined in step S3 whether the shutter button 30 has been pressed or not. If "NO", the process proceeds to step S5 to make display process of a through image. That is, the TG is instructed to carry out thin-out reading and the signal processing circuit 32 is given a process command. Due to this, part of the pixel signal is outputted from the CCD imager ~~[[12]]~~ 18. The output pixel signal is inputted to the signal processing circuit 32 through the CDS/AGC circuit

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command. Due to this, part of the pixel signal is outputted from the CCD imager ~~[[12]]~~ 18. The output pixel signal is inputted to the signal processing circuit 32 through the CDS/AGC circuit 20, A/D converter 22 and switch SW1. The signal processing circuit 32 converts the input pixel signal into a YUV signal and outputs a converted YUV signal onto the display 34. This provides display of a through-image.